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PATENT NUMBER



U.S. UTILITY Patent Application

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PATENT DATE

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APPLICATION NO. \ 09/596837	CONT/PRIOR	CLASS 438	SUBCLASS	ARTUNIT 1763	Goudrean

Visdimir Veyor

Method for fabricating microslinuctures with deep arisotropic etching of thick silicon wafers

PTO-2040 12/99

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TERMINAL		DRAWINGS		CLAIMS ALLOWED		
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